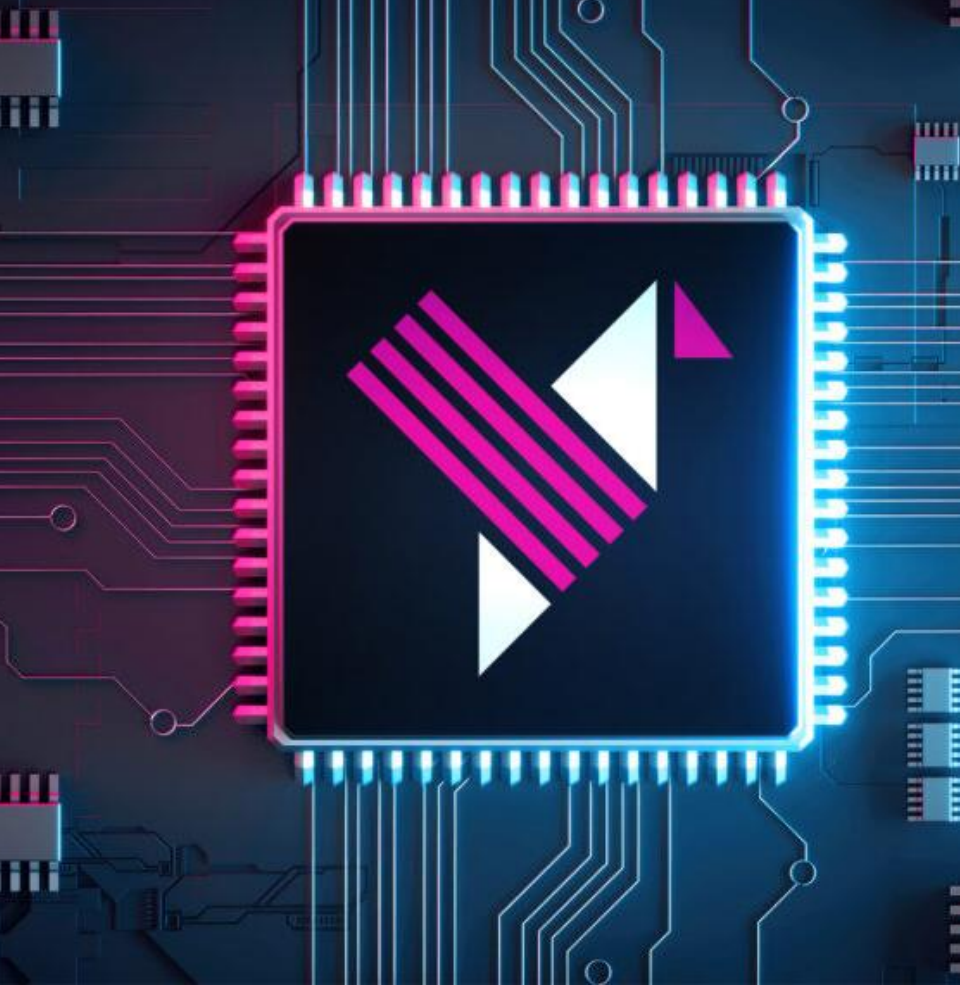




Powering the next wave of Physical AI with MIPS & GlobalFoundries

Corporate Introduction & Product Portfolio Overview
3Q25

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Introduction to MIPS

MIPS is a semiconductor company that offers platform IP, software, tools, and silicon reference platforms for physical AI products.

The adoption of AI in autonomous platforms in automotive and industrial platforms brings the world better manufacturing, vehicles, and construction.

The MIPS Atlas portfolio is the essential technology stack for Physical AI. MIPS delivers innovative solutions to the problems of low-latency real-time processing in platforms that require safety, efficiency, security, and quality.

We call this:

**Driving Intelligence Into
Action**



GlobalFoundries Acquires MIPS to Accelerate AI and Compute Capabilities

This strategic alignment validates the MIPS product vision, and accelerates our mission to power the next generation of intelligent, real-time platforms.

MIPS will continue to operate as a standalone business and serve its customers across a broad range of technologies.

The acquisition expands GF's portfolio with advanced RISC-V processor IP and software tools, enhancing its offerings for real-time computing in sectors like autonomous mobility, industrial automation, datacenters, and the intelligent edge.

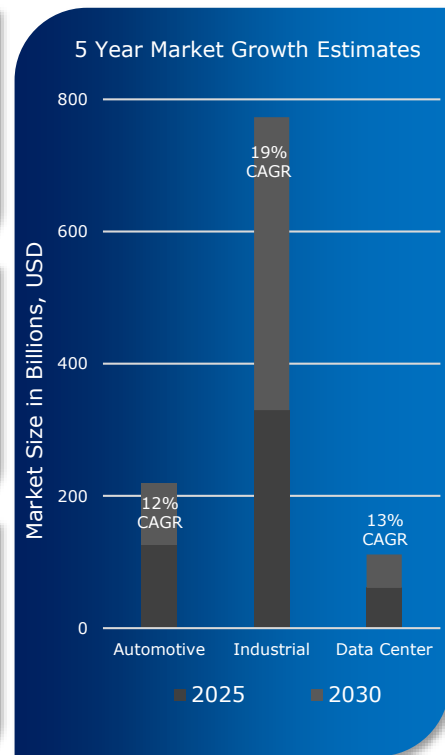
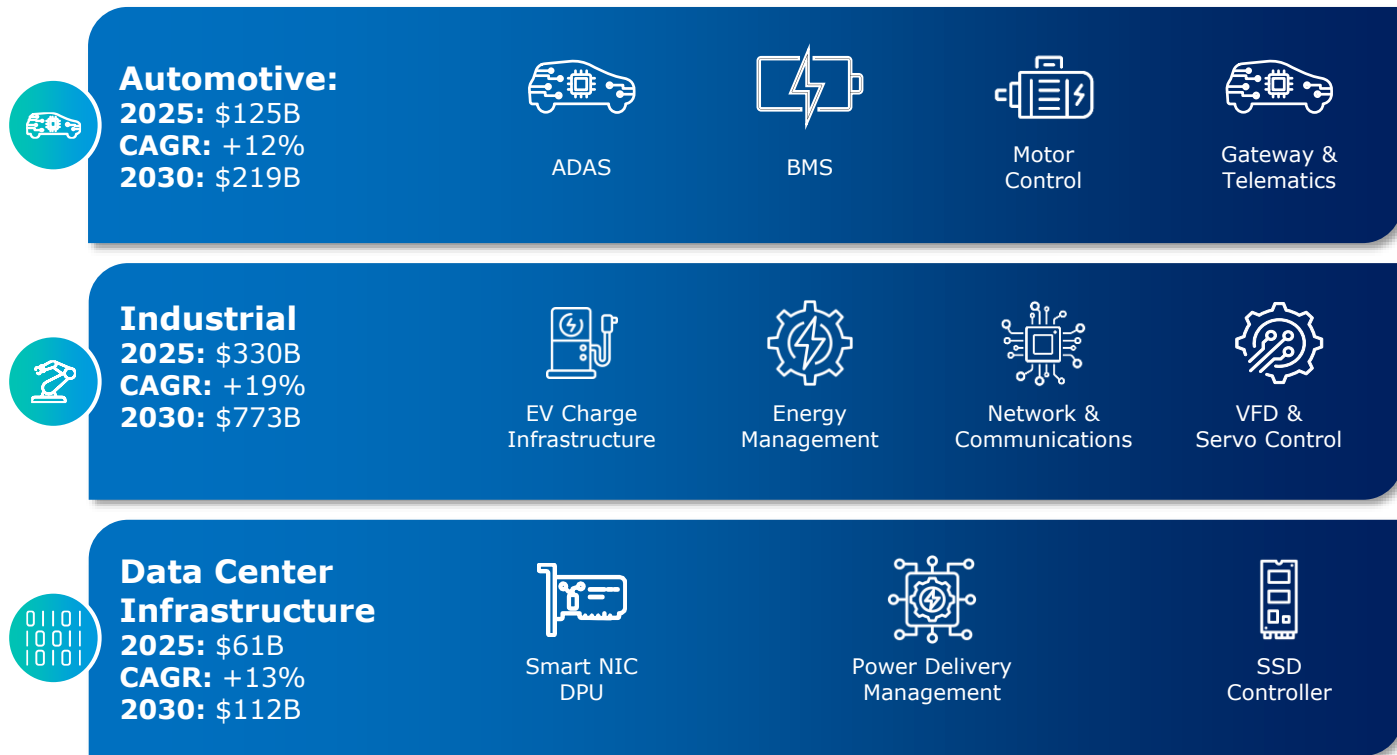
MIPS brings a 40-year legacy of RISC-based innovation, now centered on the open RISC-V instruction set architecture, with a focus on scalable, efficient compute IP.

MIPS recently launched the Atlas portfolio, a suite of compute cores for real-time and AI edge processing, and Atlas Explorer, a virtual platform for early-stage design optimization.

GF's global manufacturing and differentiated process technologies will complement MIPS' processor IP and software tools, enabling faster innovation and smarter scaling.

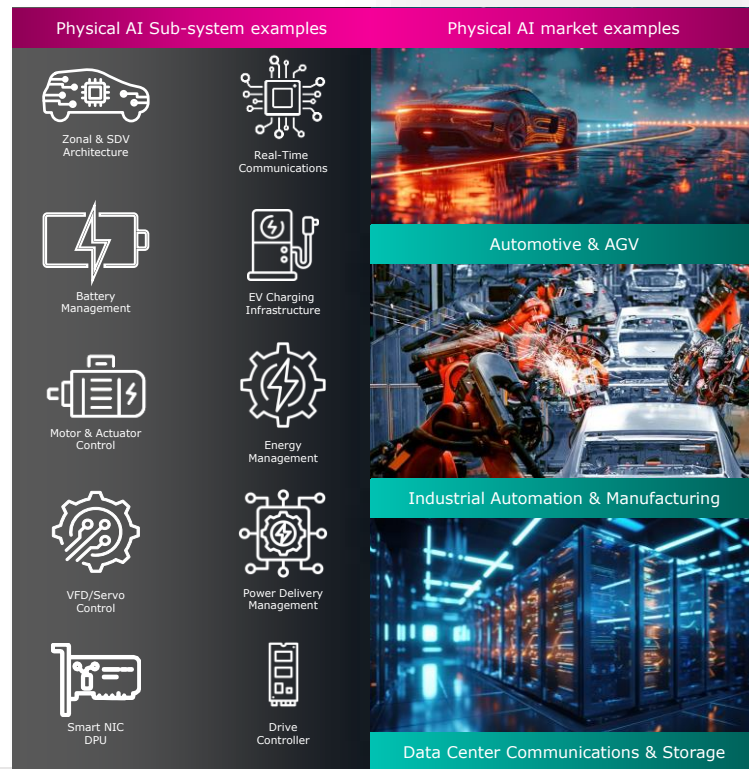
Growth Markets & Applications

AI Integration & Edge AI Opportunities Creating A >\$1T Market by 2030



Physical AI Empowers Robot Autonomy

Sense, Think, Act, & Communicate in Dynamic, Unpredictable Environments

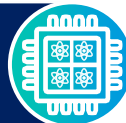


Building Compute Platforms for the Physical AI Era



Application Focused Silicon:

- MIPS reference platforms optimized for best-in-class performance, efficiency, & cost turnkey enablement
- Bespoke SoCs co-developed with customer needs, based on MIPS processor IP & SoC modules



Advanced Optimization:

- Atlas Explorer virtual platform enables pre-RTL evaluation, pre-silicon optimization, & platform digital twins
- Application Performance Packs accelerate safety, security, and domain-specific workload development



MIPS Atlas Portfolio of Platform IP:

- Multithreaded architecture delivers more performance in less area for optimal silicon use
- Application specific compute enhancements for class-leading performance, power, & area (PPA)



Enabling Success in Growth Markets:

- Trusted, reliable global manufacturing partners with low-power, optimized process technology
- Commercial and open-source ecosystem support for MIPS, RISC-V, and Physical AI adoption



Open RISC-V Instruction Set Architecture (ISA)

- Easy to adopt open specification with robust ecosystem of commercial & open-source toolchains
- Modular for building differentiation & acceleration while ensuring compatibility & avoiding lock-ins



8 Product Generations:

- Silicon proven in advanced process technology for safety-critical applications & data center infrastructure
- Supporting world-class, global enterprises & their customers in Automotive & Cloud today



MIPS Business Units



Processor IP

Atlas Processor IP Portfolio

- Microcontrollers and Embedded Applications processors
- Best-in-class solutions built on open, RISC-V-based architectures
- Application enhanced instructions
- Functional safety certified
- Performance, Power, and Area optimized designs

GM: Drew Barbier



Custom Silicon

Customer Driven Application Specific SoCs

- MIPS performance-tuned cores & subsystems
- Use-case driven features
- Cost-optimized design & package
- Auto/Industrial Qualifications
- Safety-Capable Platforms
- Post-Quantum Security capabilities

GM: Vasanth Waran









Software

Atlas Software


- Virtual Platforms for MIPS IP
- Software & Hardware Co-Design
- Application Focused Performance Packs
 - Functional Safety
 - Real-Time Control
 - & More
- Services
 - Application Customization
 - Security & Long-Term Support

MIPS Atlas Portfolio

	P8700	<ul style="list-style-type: none"> Scalable Applications Processor with multithreading (SMT) The only multi-threaded RISC-V out-of-order core with ASIL-B certification
	I8500	<ul style="list-style-type: none"> Scalable power-efficient Applications Processor with SMT The only multi-threaded RISC-V core capable of executing four threads
	M8500	<ul style="list-style-type: none"> High performance microcontroller for sub-10μS control loops and real-time interrupt processing ~33% less area than dual-IP solutions
	M8500 Drive	<ul style="list-style-type: none"> Best in class Real time & Control loop performance – providing best power/performance & cost for system designers Up to 6x better performance than proprietary solutions
	I8500 Gateway	<ul style="list-style-type: none"> Grounds-up Gateway solution bridging legacy solutions with state-of-the-art SDV/SOA solutions Up to 4x better performance for Networking workloads



Atlas Software



- Atlas Explorer**
 - Pre-silicon evaluation of system level performance
 - Enables shift-left HW/SW optimization
 - Continuous integration & development
- MIPS 8th gen. processors are RISC-V ISA compliant**
 - Easy adoption of MIPS defined instructions and optimizations
 - RISC-V modular approach enables MIPS differentiation at the ISA level without sacrificing compatibility
 - Active leadership & participation in RVI task groups to drive features and compatibility
- MIPS Software Packs**
 - Safety SDKs (ISO 26262, IEC 61508, EN 50128/50657 & more)
 - Per Platform SDKs
 - Workload Specific Optimization Libraries
- Open-Source Contributions**
 - Linux Kernel for MIPS compute enhancements, devices, and RISC-V optimizations
 - U-Boot, BuildRoot; FreeRTOS, MCUBoot, etc.
 - Compilers (GCC/LLVM)
- Commercial Ecosystem Enablement**
 - EDA
 - OS Partners
 - Safety Partners
 - Security Partners

MIPS x GF: Powering The Next Wave of Physical AI



40 Years Of History

- Founded in 1985
- MIPS part of '80s RISC Revolution
- Multithreading Innovation
- Domain-focused Cores
SGI, Sony, Nintendo, NEC, Cisco & more



MIPS 8th Generation Atlas Portfolio for real-time compute



Open & Modular RISC-V ISA



GlobalFoundries Acquires in MIPS 2025 to accelerate AI & compute capabilities



Build Better Products

- Modern Use Cases Supported at Scale
- 7 Generations of Mobileye EyeQ Platforms for ADAS
- 100M units shipped
- 40+ Auto OEMs
- 70% of ADAS today

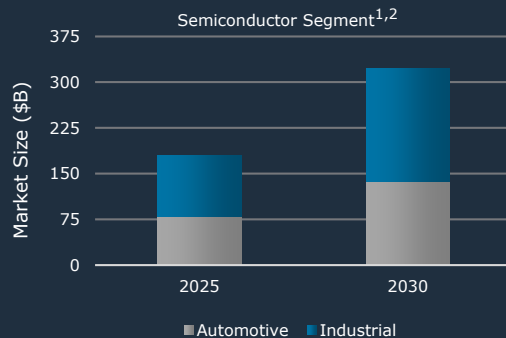


- 2 Generations of Smart NIC/Data Processing Unit (DPU) at Tier 1 Cloud Provider



Market Trends

- Adoption of AI
- Real-time compute is driving growth³
- Physical AI Market:
 - >\$1T by 2030⁴
 - \$4.7T by 2050⁵
- 5-Year CAGR >10%:





Endnotes

Market Size and Growth data sources :

- ADAS: [Future Market Insights](#)
- BMS: [Markets and Markets](#)
- EV Motor control: [Mordor Intelligence](#)
- Automotive gateway & connectivity: [Future Market Insights – Connectivity](#); [Yahoo Finance – Communication Technology](#)
- EV Charging Infrastructure: [IEA Global EV Outlook 2025](#); [StartUs Insights](#)
- Industrial Energy Management Systems: [GMI Insights](#)
- Autonomous Production Environments: [GlobeNewswire – Research and Markets](#)
- Variable Frequency Drives: [MarketsandMarkets](#)
- Smart NIC & DPU: [Business Research Insights](#); [Smart NICs Summit Presentation](#)
- Data Center Power Management: [The Business Research Company](#); [Research and Markets](#)
- SSD Controller: [The Business Research Company](#); [Fortune Business Insights](#); [Coherent Market Insights](#)

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