MIPS

Corporate Overview

Freedom to Innovate Compute

MIPS is a compute IP company with more than two decades of experience. Breaking down the barriers of closed and in-flexible architectures, MIPS designs efficient and configurable compute cores for automotive, data center and embedded systems.

Vision: Accelerate compute density in the automotive, data center and embedded markets

Founded in 1984, MIPS-based chips can be found in billions of devices and systems around the world. Fast forward to today where the new compute era is marked by unprecedented technological advancements and growing demands of data-driven and AI-enabled systems. This is where traditional architectures are slowing down innovation as they struggle to meet high-performance and efficient high-density compute requirements.

MIPS is delivering a new approach that gives customers the freedom to innovate compute by enabling efficient data movement, real-time processing and low latency. This encompasses the freedom to select an open architecture, break down system bottlenecks, and not be forced to use a single ISA. MIPS solutions are based on open source industry standards such as the RISC-V architecture, enabling customers to innovate faster, more efficiently and push the boundaries of what's achievable.

Leadership Team

MIPS is led by CEO Sameer Wasson, who joined in 2023 to transition the company into its next phase of growth. The team has a breadth of experience in the semiconductor, automotive, data center, and embedded markets, previously working for companies such as Analog Devices, Intel, SiFive, SK Hynix, Synopsys, and Texas Instruments. View leadership team [bios](.).

"The opportunity for compute innovation is huge. MIPS cores give customers the freedom to seamlessly accelerate and expand compute power at higher density, assuring optimal performance and resource utilization. This means safer and software upgradable cars, more power-efficient data centers, and much more capable embedded systems."
Sameer Wasson, CEO.

Locations

Headquartered in San Jose, MIPS has offices in Dallas, Austin, and Bangalore (INDIA).

Key Facts

- Over 8.5 Billions of MIPS chips shipped to-date.
- 70% automotive ADAS compute deployments run on MIPS today across many global Auto OEMs.
- Three design centers/COEs (Center of Excellence) in San Jose (CA), Dallas (TX) and Bangalore (INDIA) today, additionally started Austin (TX) design center in 2024.
- Over 90 employees to-date, plan to double capacity in 2024.
- Diverse talent pool with industry experts in design, high-performance micro-architectures, performance modeling, toolchains, DFT, and other areas.
- Rapidly expanding the MIPS RISC-V customer base in 2024, specifically targeting automotive, data centers, and embedded markets, augmenting existing MIPS production royalty OEMs.
- Market driven and focused on solving difficult requirements through a mix of general-purpose compute, optimized data movement, and specialization through User Defined Instructions (UDI) and custom interfaces.